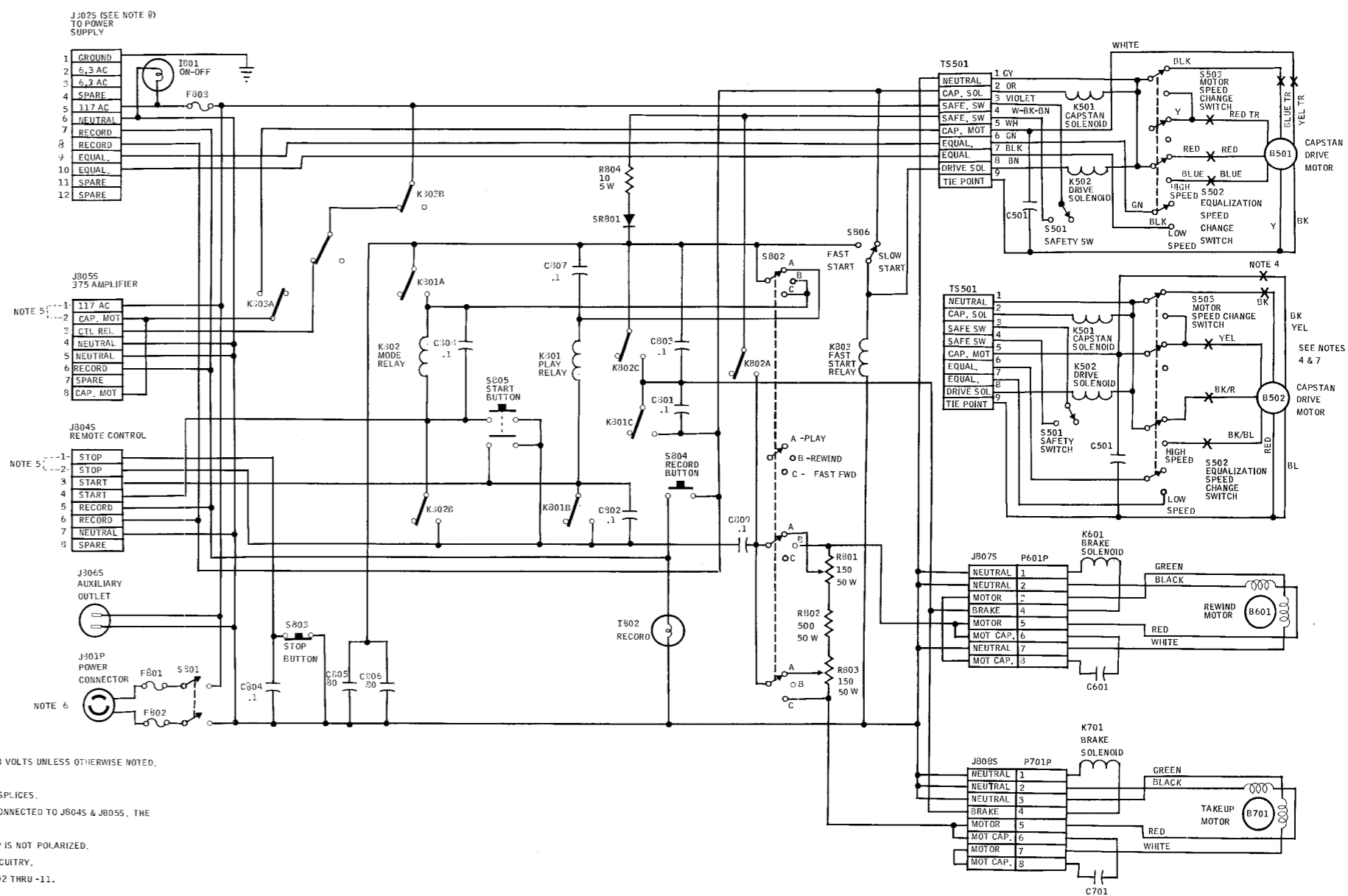
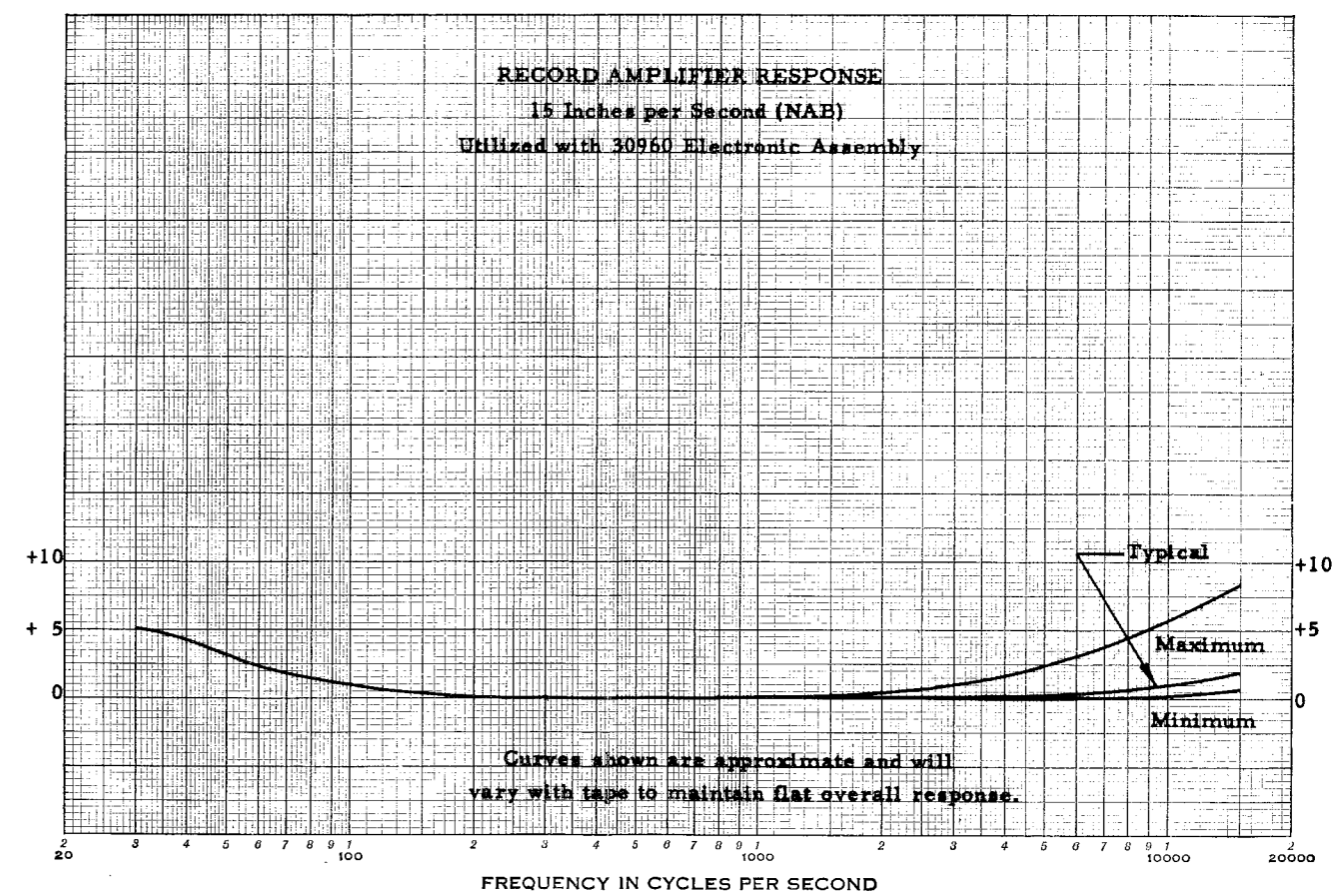
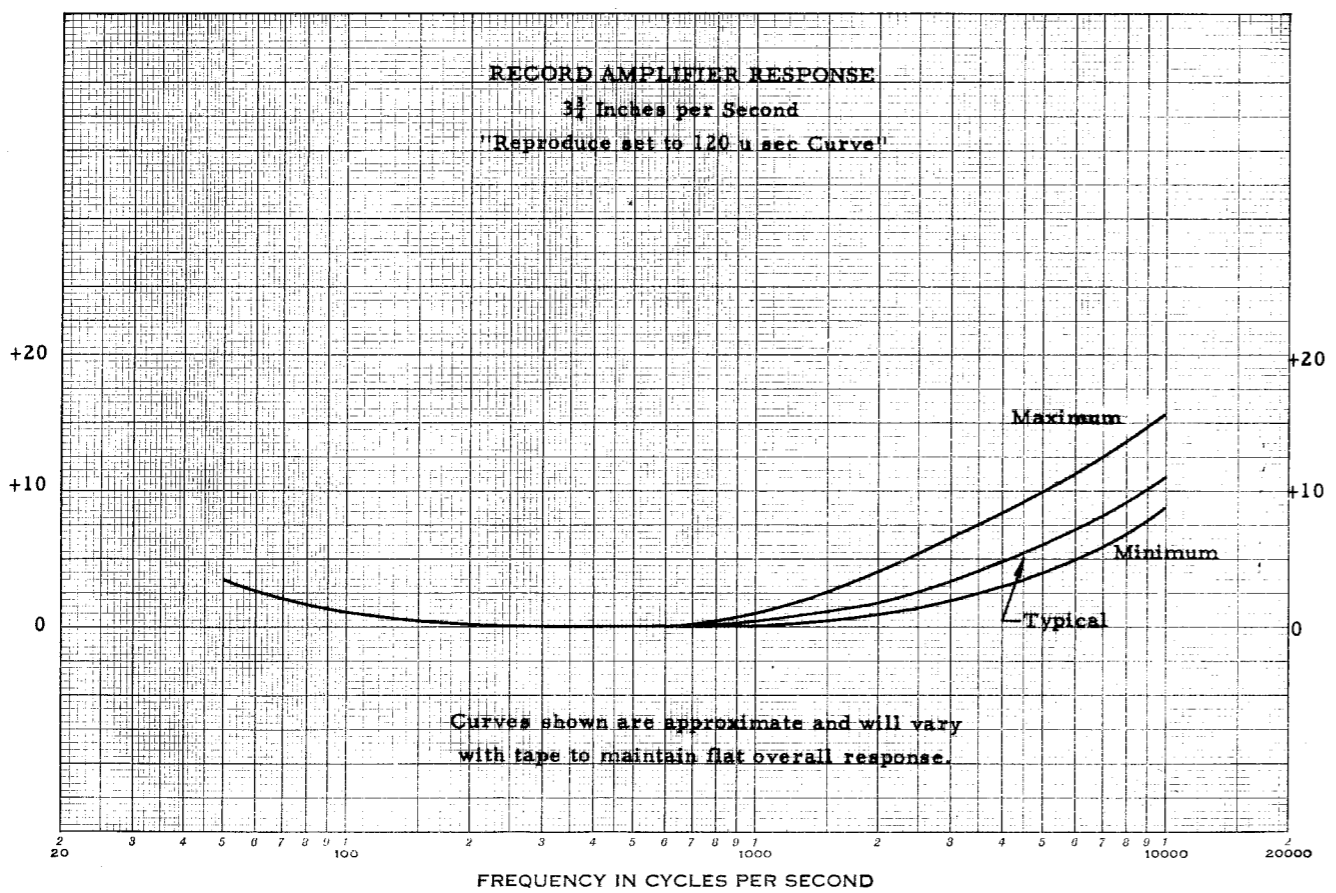
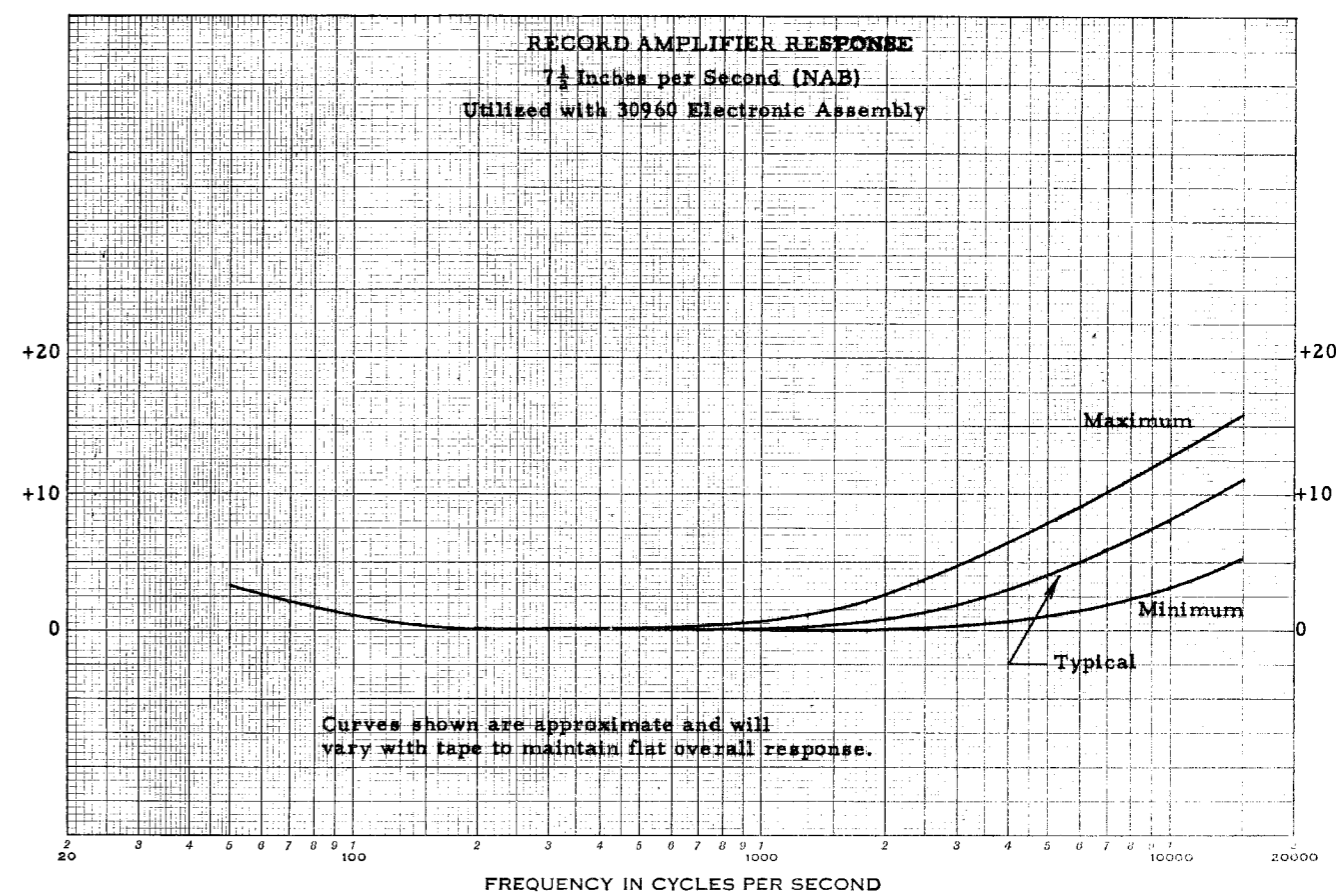
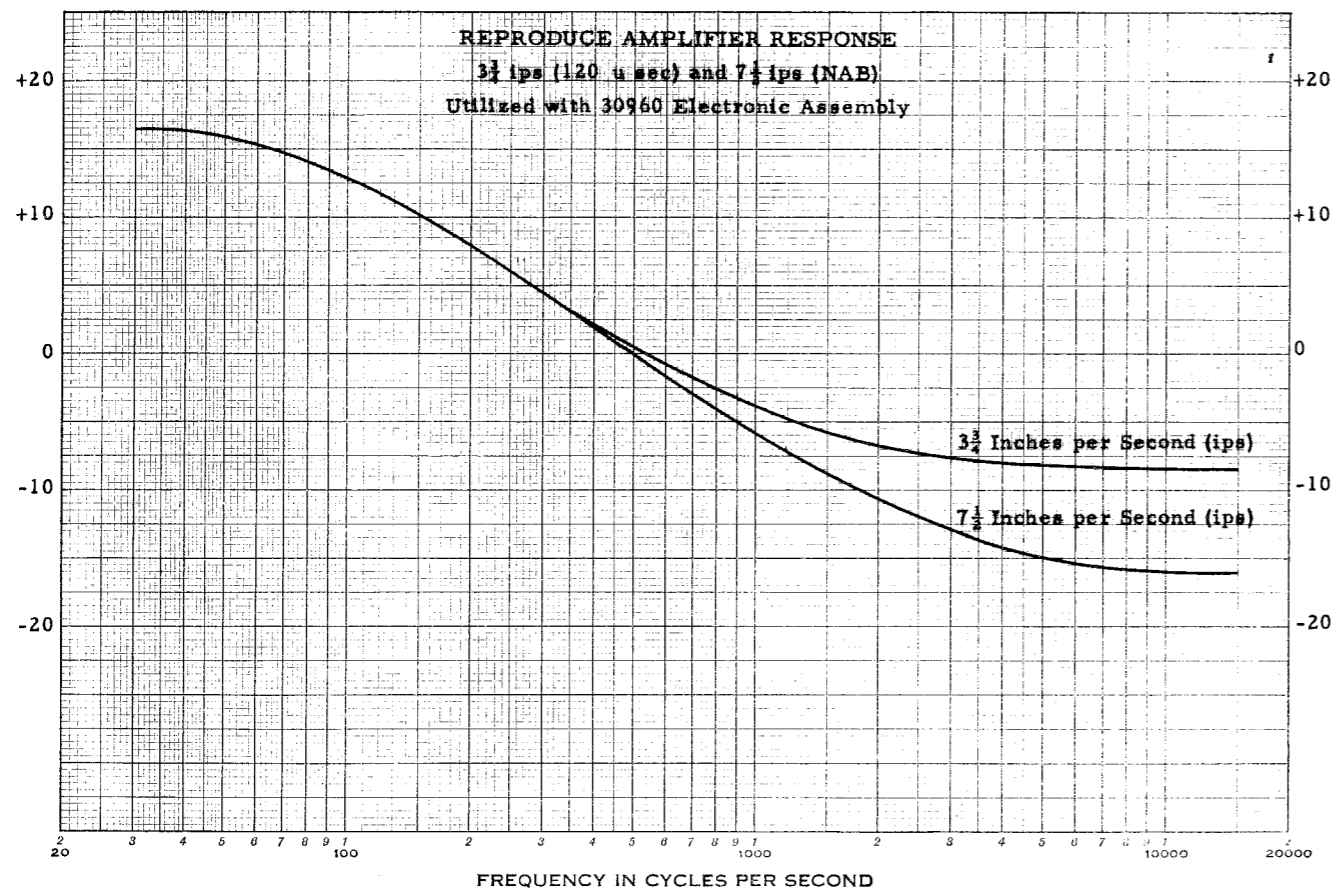
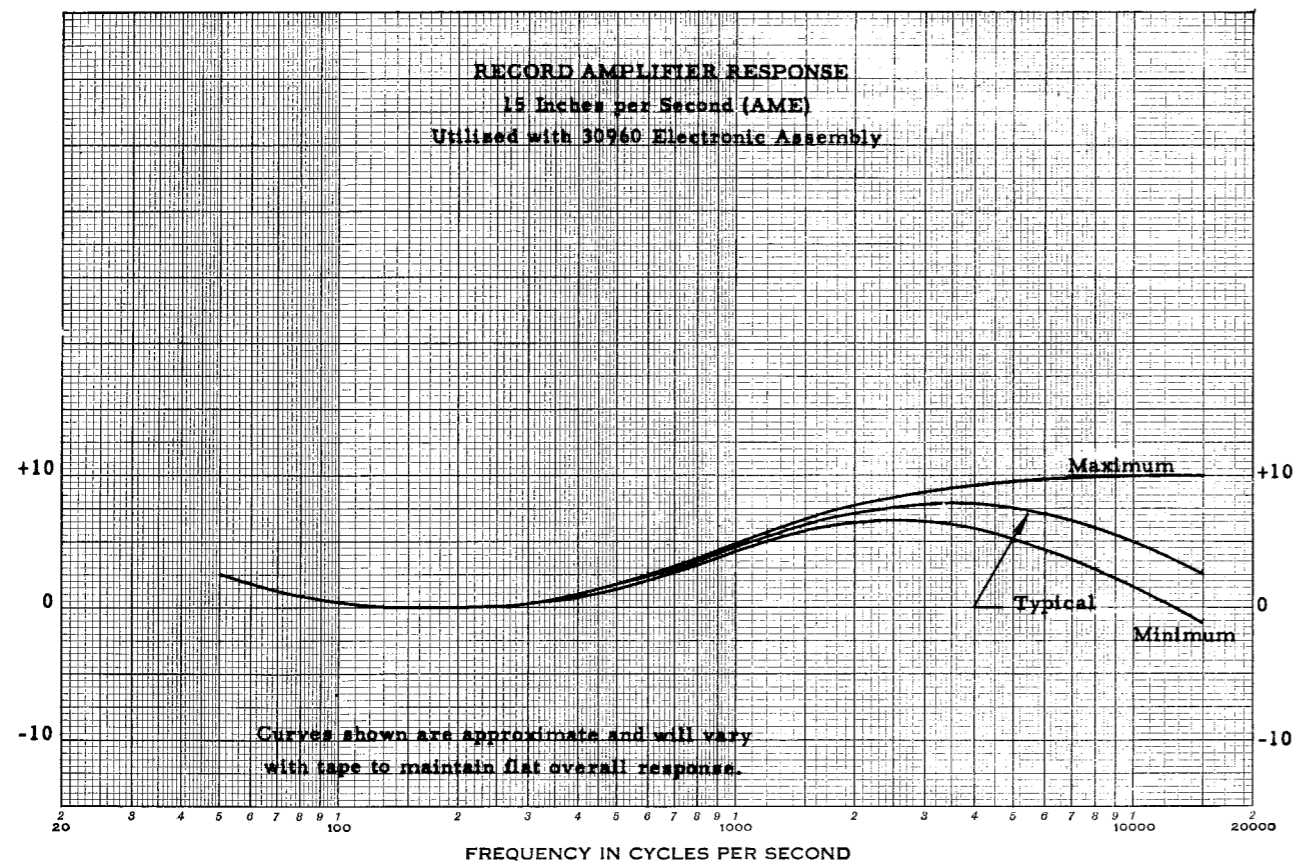
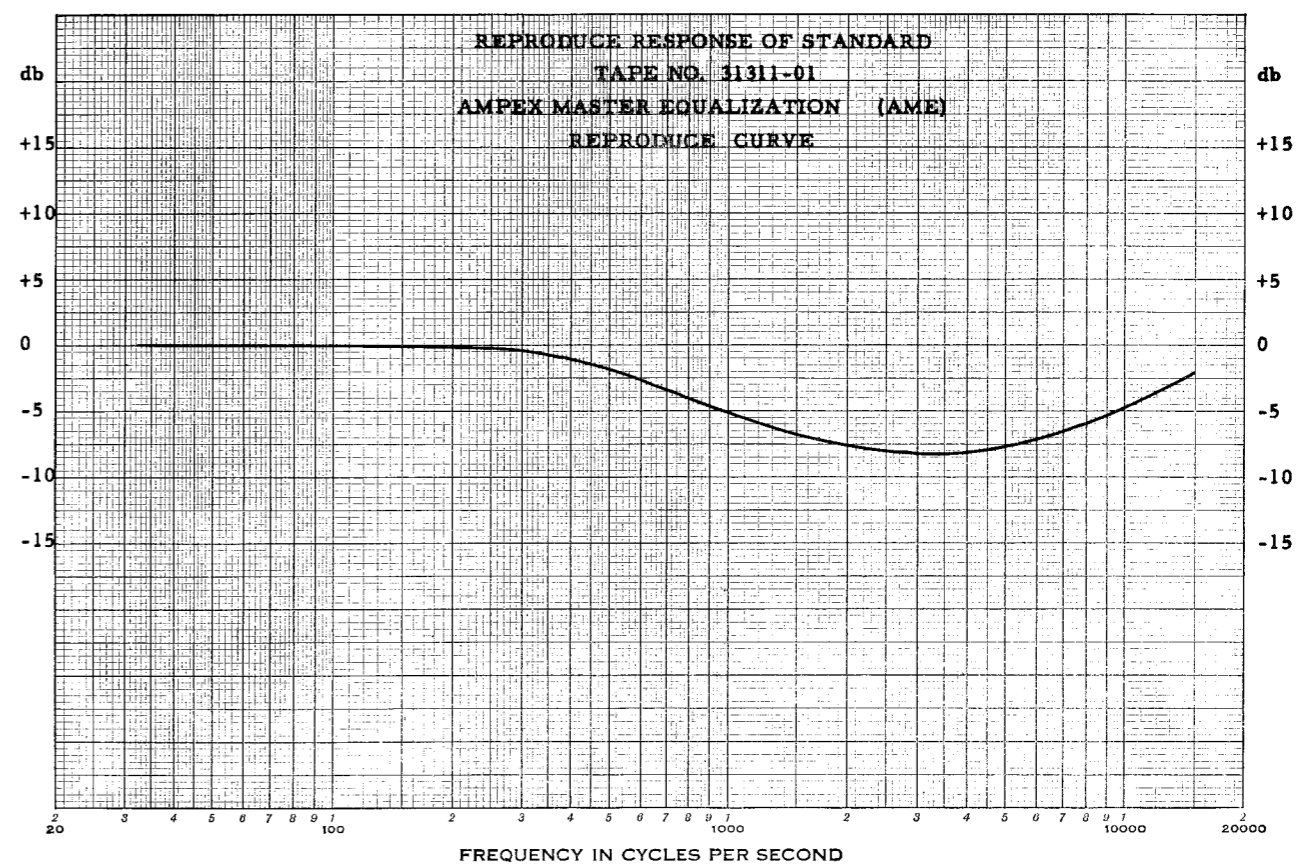
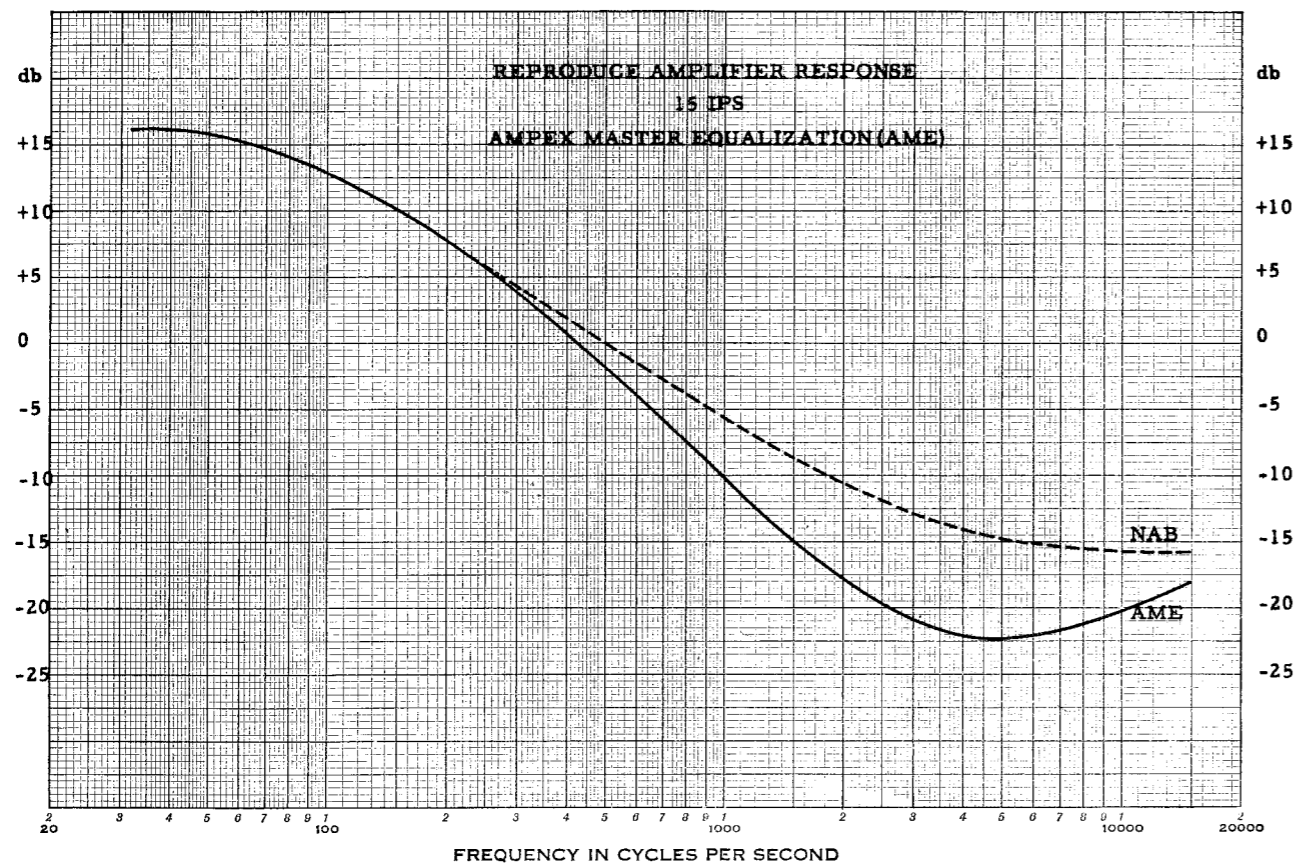


SCHEMATIC DIAGRAM
 SERIES 300 TAPE TRANSPORT
 CONTROL CIRCUITS
 1882T

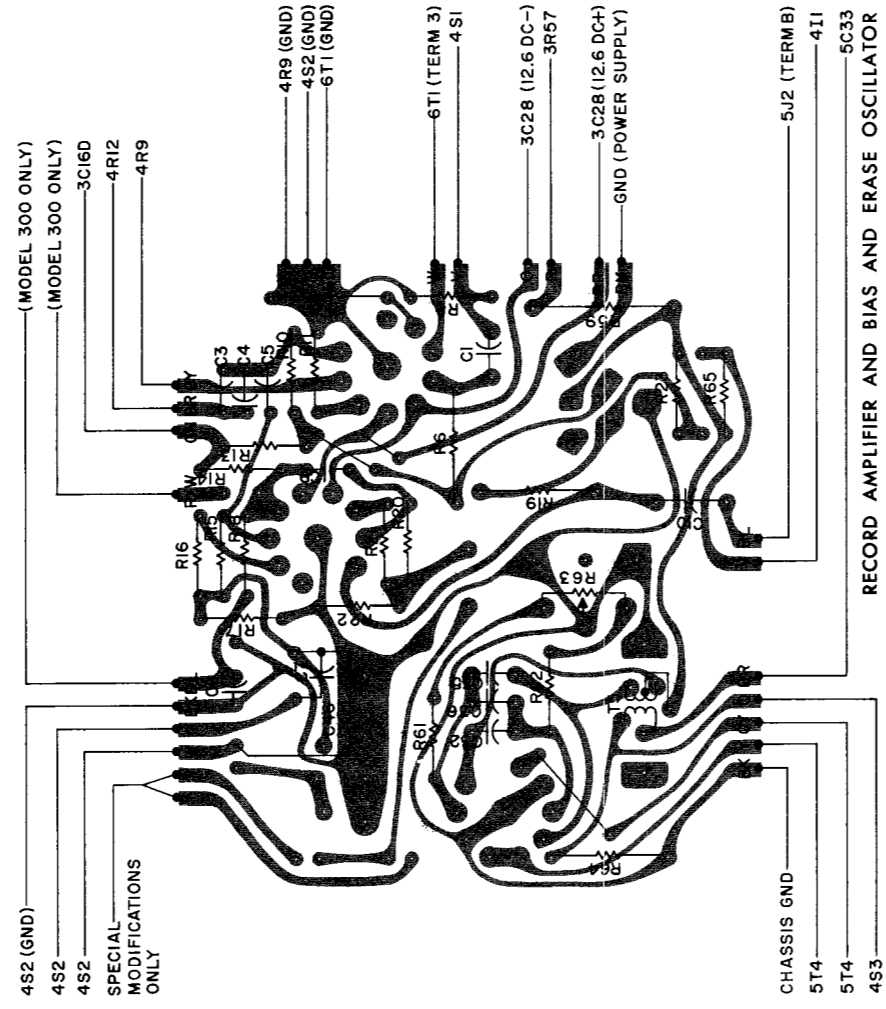


- NOTES:
1. ALL RESISTORS IN OHMS.
 2. ALL CAPACITORS IN MICROFARADS, 600 VOLTS UNLESS OTHERWISE NOTED.
 3. ALL RELAYS SHOWN DEENERGIZED.
 4. "X"'S REPRESENT MOTOR DISCONNECT SPLICES.
 5. UNIT IS SUPPLIED WITH A-567 PLUGS CONNECTED TO J804S & J805S. THE A-567 PLUG IS STRAPPED AS SHOWN.
 6. ON 1/4 INCH TAPE TRANSPORTS, J801P IS NOT POLARIZED.
 7. ALTERNATE CAPSTAN DRIVE MOTOR CIRCUITRY.
 8. I801 IS CONNECTED TO 2 & 3 ON 7784-02 THRU -11.

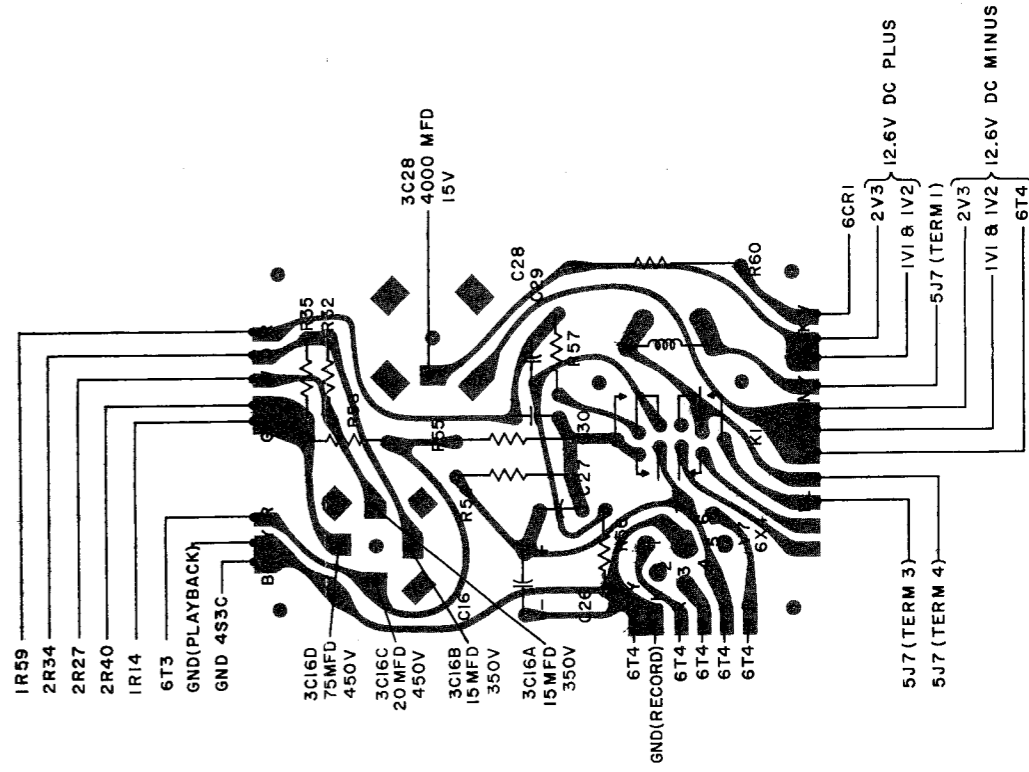




UTILIZED WITH 30960 ELECTRONIC ASSEMBLIES ONLY



RECORD AMPLIFIER AND BIAS AND ERASE OSCILLATOR
PARTS LOCATION

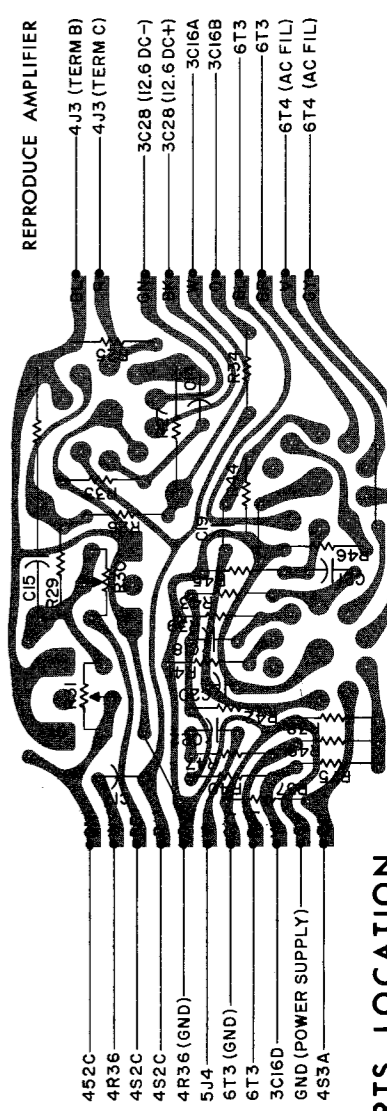


UTILIZED WITH 30960 ELECTRONIC ASSEMBLIES ASSEMBLIES ONLY

LEGEND

THE FIRST NUMBERS SIGNIFY THE LOCATION.

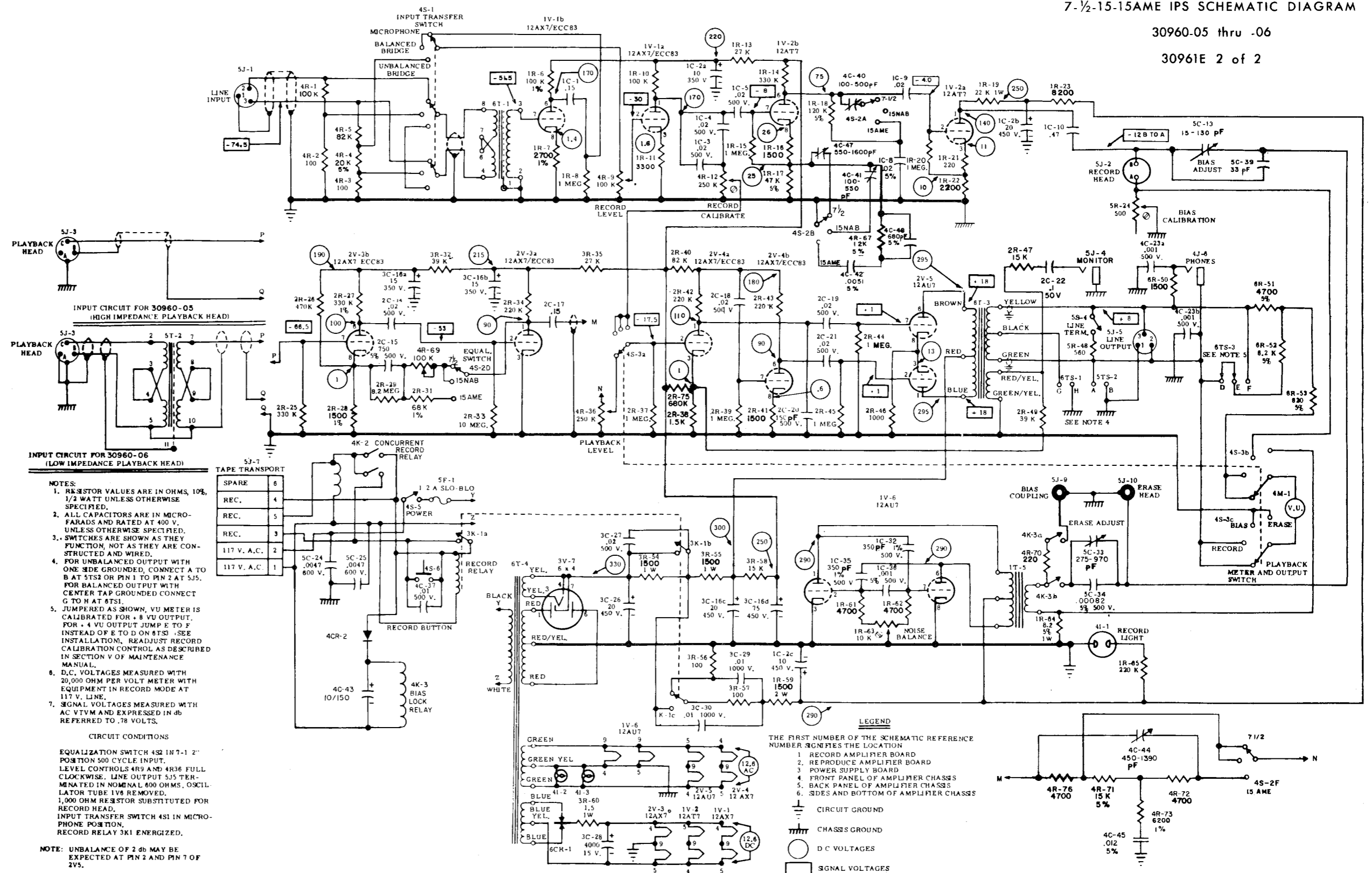
1. RECORD AMPLIFIER BOARD
2. REPRODUCE AMPLIFIER BOARD
3. POWER SUPPLY BOARD
4. FRONT PANEL OF AMP. CHASSIS
5. BACK PANEL OF AMP. CHASSIS
6. SIDES AND BOTTOM



REPRODUCE AMPLIFIER
PARTS LOCATION
POWER SUPPLY AND REPRODUCE AMPLIFIER

ELECTRONIC ASSEMBLY
2,3 and 4 TRACK RECORDER/REPRODUCER
7-1/2-15-15AME IPS SCHEMATIC DIAGRAM

30960-05 thru -06
30961E 2 of 2



- NOTES:
1. RESISTOR VALUES ARE IN OHMS, 10%, 1/4 WATT UNLESS OTHERWISE SPECIFIED.
 2. ALL CAPACITORS ARE IN MICRO-FARADS AND RATED AT 400 V. UNLESS OTHERWISE SPECIFIED.
 3. SWITCHES ARE SHOWN AS THEY FUNCTION, NOT AS THEY ARE CONSTRUCTED AND WIRED.
 4. FOR UNBALANCED OUTPUT WITH ONE SIDE GROUNDING, CONNECT A TO B AT 5TS2 OR PIN 1 TO PIN 2 AT 5J5. FOR BALANCED OUTPUT WITH CENTER TAP GROUNDING CONNECT G TO H AT 6TS1.
 5. JUMPED AS SHOWN, VU METER IS CALIBRATED FOR .8 VU OUTPUT. FOR .4 VU OUTPUT JUMP E TO F INSTEAD OF E TO D ON 6TS3. SEE INSTALLATION, READJUST RECORD CALIBRATION CONTROL AS DESCRIBED IN SECTION V OF MAINTENANCE MANUAL.
 6. D.C. VOLTAGES MEASURED WITH 20,000 OHM PER VOLT METER WITH EQUIPMENT IN RECORD MODE AT 117 V. LINE.
 7. SIGNAL VOLTAGES MEASURED WITH AC VTVM AND EXPRESSED IN db REFERRED TO .78 VOLTS.

CIRCUIT CONDITIONS

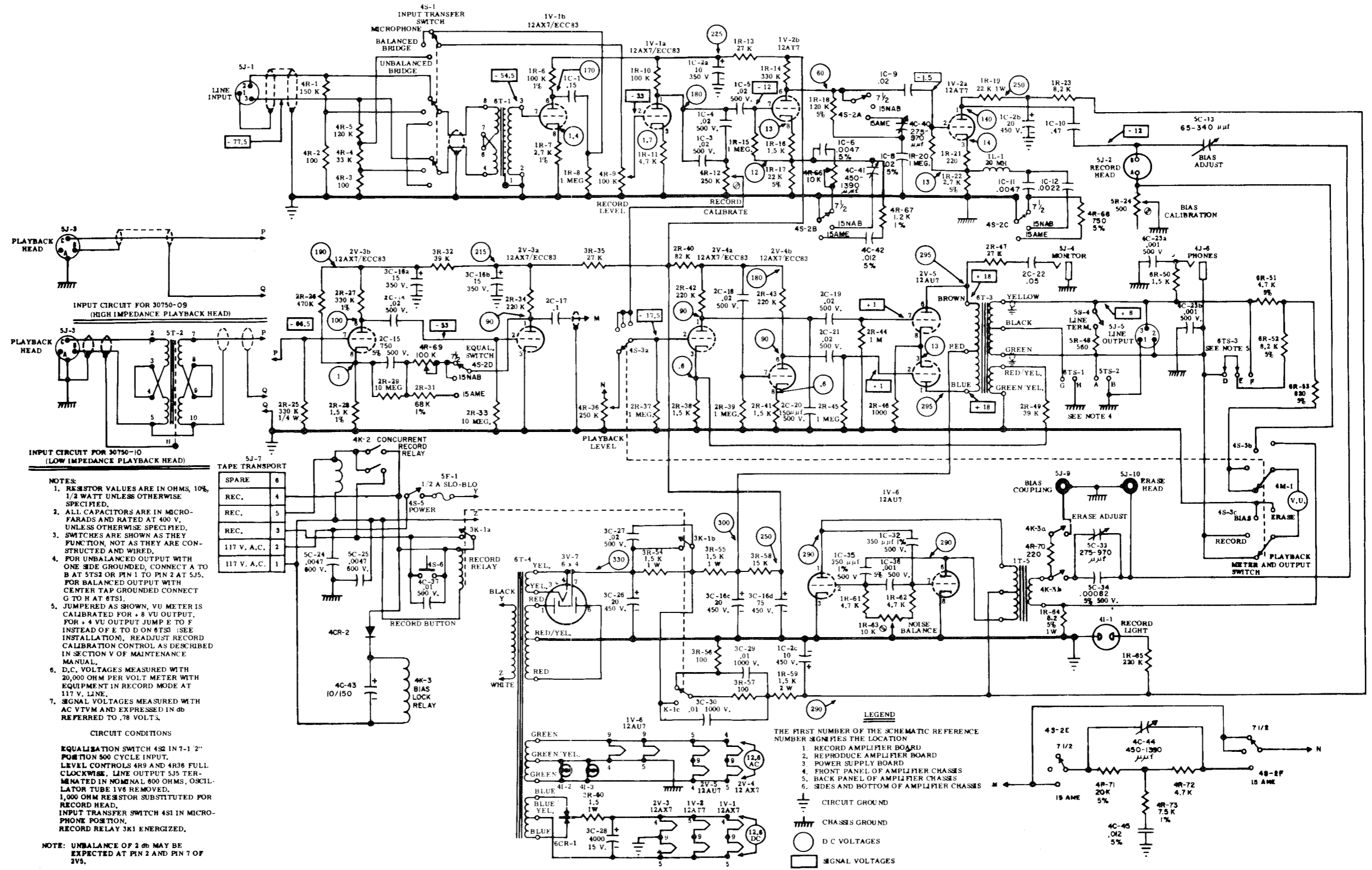
EQUALIZATION SWITCH 4S2 IN 7-1 2" POSITION 500 CYCLE INPUT.
LEVEL CONTROLS 4R5 AND 4R36 FULL CLOCKWISE. LINE OUTPUT 5J5 TERMINATED IN NOMINAL 600 OHMS. OSCILLATOR TUBE 1V6 REMOVED.
1,000 OHM RESISTOR SUBSTITUTED FOR RECORD HEAD.
INPUT TRANSFER SWITCH 4S1 IN MICROPHONE POSITION,
RECORD RELAY 3K1 ENERGIZED.

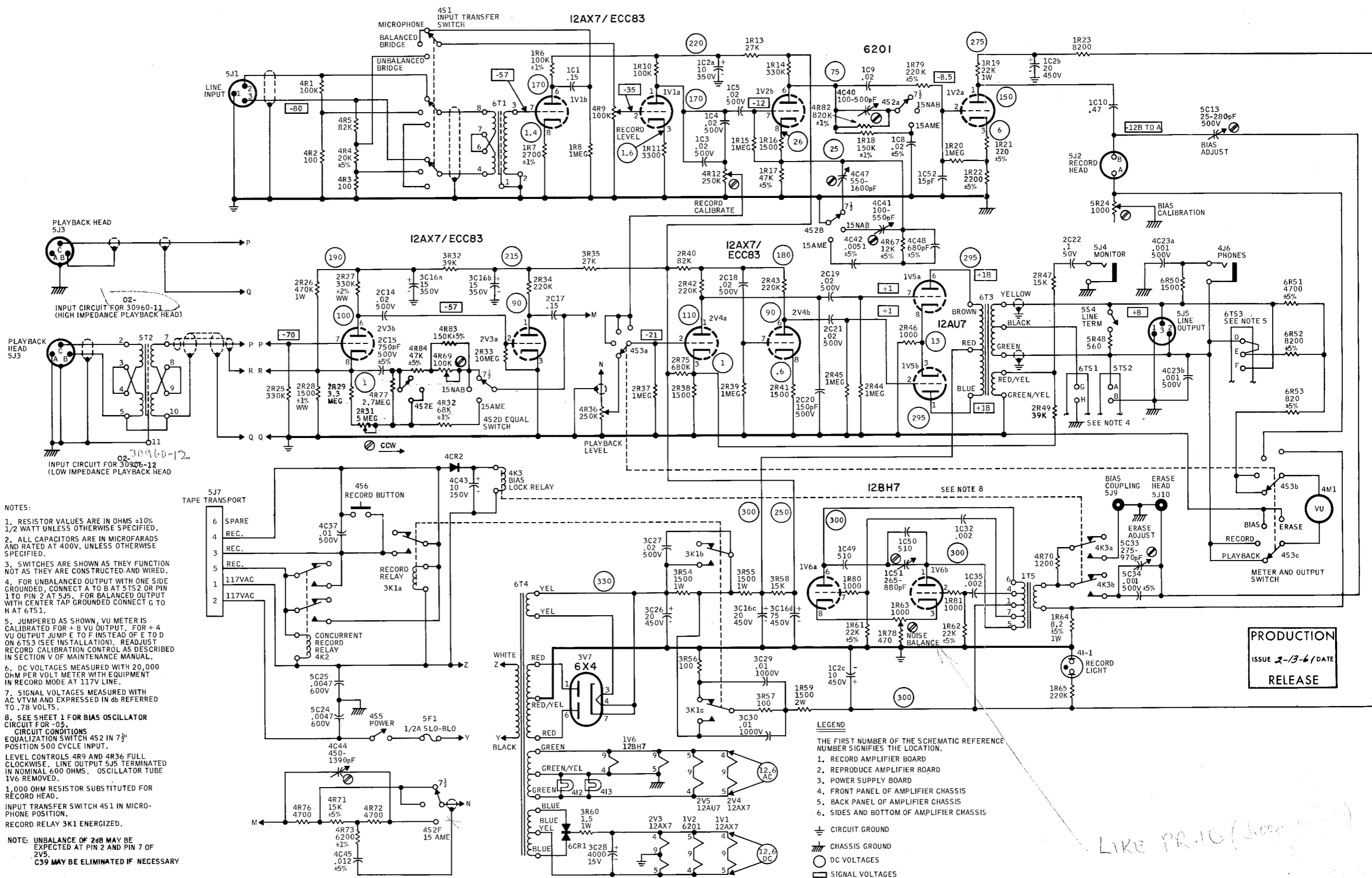
NOTE: UNBALANCE OF 2 db MAY BE EXPECTED AT PIN 2 AND PIN 7 OF 2V5.

5J-7 TAPE TRANSPORT	
SPARE	6
REC.	4
REC.	5
REC.	3
117 V. A.C.	2
117 V. A.C.	1

- LEGEND
- THE FIRST NUMBER OF THE SCHEMATIC REFERENCE NUMBER SIGNIFIES THE LOCATION
1. RECORD AMPLIFIER BOARD
 2. REPRODUCE AMPLIFIER BOARD
 3. POWER SUPPLY BOARD
 4. FRONT PANEL OF AMPLIFIER CHASSIS
 5. BACK PANEL OF AMPLIFIER CHASSIS
 6. SIDES AND BOTTOM OF AMPLIFIER CHASSIS
- CIRCUIT GROUND
 CHASSIS GROUND
 D.C. VOLTAGES
 SIGNAL VOLTAGES

ELECTRONIC ASSEMBLY
2,3 and 4 TRACK RECORDER/REPRODUCER
7-1/2-15-15AME IPS SCHEMATIC DIAGRAM
30750-09 thru -10
30802L 2 of 2





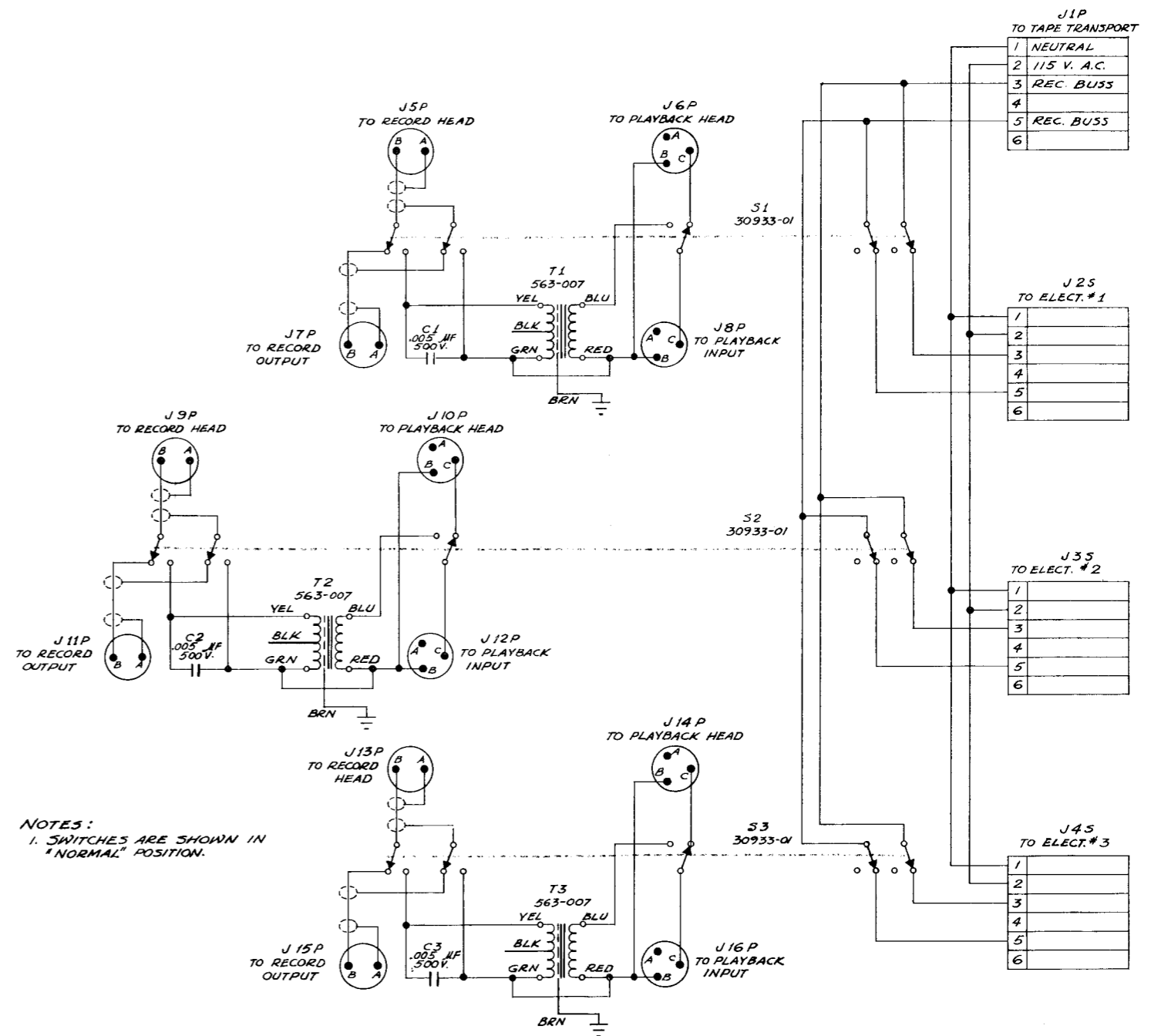
- NOTES:**
1. RESISTOR VALUES ARE IN OHMS $\pm 10\%$ 1/2 WATT UNLESS OTHERWISE SPECIFIED.
 2. ALL CAPACITORS ARE IN MICROFARADS AND RATED AT 400V, UNLESS OTHERWISE SPECIFIED.
 3. SWITCHES ARE SHOWN AS THEY FUNCTION NOT AS THEY ARE CONSTRUCTED AND WIRED.
 4. FOR UNBALANCED OUTPUT WITH ONE SIDE GROUND, CONNECT A TO B AT 5T2 OR PIN 1 TO PIN 2 AT 5J5. FOR BALANCED OUTPUT WITH CENTER TAP GROUND, CONNECT G TO H AT 6T51.
 5. JUMPERED AS SHOWN, VU METER IS CALIBRATED FOR +8 VU OUTPUT. FOR +4 VU OUTPUT JUMP E TO F INSTEAD OF E TO D ON 6T53 (SEE INSTALLATION). READJUST RECORD CALIBRATION CONTROL AS DESCRIBED IN SECTION V OF MAINTENANCE MANUAL.
 6. DC VOLTAGES MEASURED WITH 20,000 OHM PER VOLT METER WITH EQUIPMENT IN RECORD MODE AT 117V LINE.
 7. SIGNAL VOLTAGES MEASURED WITH AC VTVM AND EXPRESSED IN db REFERRED TO 78 VOLTS.
 8. SEE SHEET 1 FOR BIAS OSCILLATOR CIRCUIT FOR -05.
- CIRCUIT CONDITIONS**
EQUALIZATION SWITCH 4S2 IN 7 $\frac{1}{2}$ POSITION 500 CYCLE INPUT.
LEVEL CONTROLS 4R9 AND 4R36 FULL CLOCKWISE. LINE OUTPUT 5J5 TERMINATED IN NOMINAL 600 OHMS. OSCILLATOR TUBE 1V6 REMOVED.
1,000 OHM RESISTOR SUBSTITUTED FOR RECORD HEAD.
INPUT TRANSFER SWITCH 4S1 IN MICROPHONE POSITION.
RECORD RELAY 3K1 ENERGIZED.
- NOTE:** UNBALANCE OF 24B MAY BE EXPECTED AT PIN 2 AND PIN 7 OF 2V5.
C39 MAY BE ELIMINATED IF NECESSARY

- LEGEND**
- THE FIRST NUMBER OF THE SCHEMATIC REFERENCE NUMBER SIGNIFIES THE LOCATION.
1. RECORD AMPLIFIER BOARD
 2. REPRODUCE AMPLIFIER BOARD
 3. POWER SUPPLY BOARD
 4. FRONT PANEL OF AMPLIFIER CHASSIS
 5. BACK PANEL OF AMPLIFIER CHASSIS
 6. SIDES AND BOTTOM OF AMPLIFIER CHASSIS
- ⊕ CIRCUIT GROUND
⊖ CHASSIS GROUND
⊖ DC VOLTAGES
○ SIGNAL VOLTAGES

PRODUCTION
ISSUE 2-13-61 DATE
RELEASE

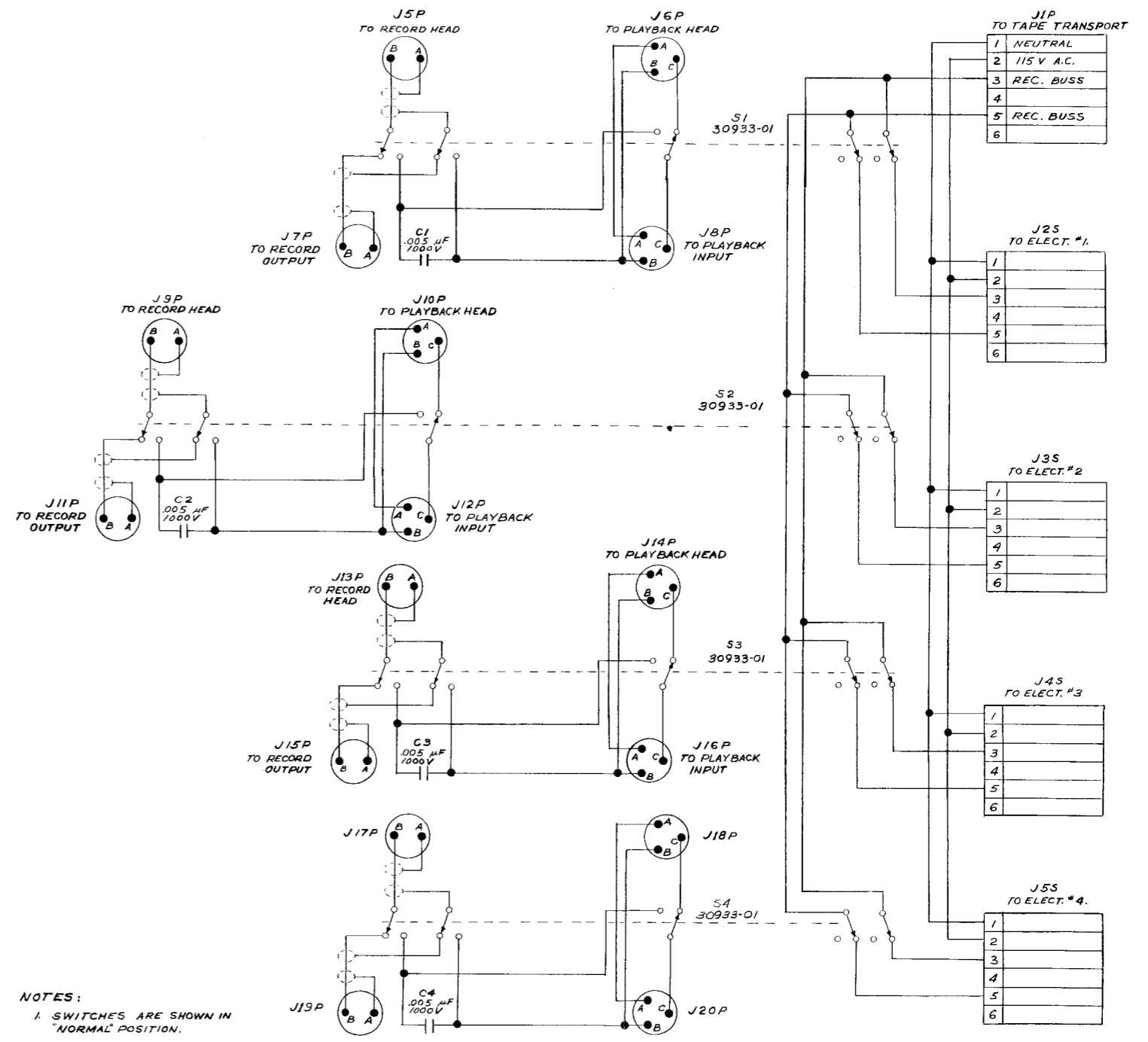
LIKE PR.10 (3000...)
30960-12
-11

SCHEMATIC DIAGRAM
3 CHANNEL SEL-SYNC
30691B



NOTES:
1. SWITCHES ARE SHOWN IN "NORMAL" POSITION.

SCHEMATIC DIAGRAM
4 CHANNEL SEL-SYNC
LOW IMPEDANCE HEAD
31009A



NOTES:
1. SWITCHES ARE SHOWN IN
"NORMAL" POSITION.